

Attorney's Docket No.: 10559-689001

Amendment to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application:

1. (Currently Amended) A method of transferring data within a computer system, comprising:

completing a first data transfer;
determining that no data transfers are being processed;
using a computer for coalescing two or more transfer requests into a second data transfer; and
releasing the a second data transfer for processing.

2. (Canceled)

3. (Currently Amended) The method of claim 1 2, further comprising:

determining there are pending transfer requests to coalesce before releasing the second ~~combined~~ data transfer.

4. (Currently Amended) The method of claim 3, wherein releasing comprises determining that the second ~~combined~~ data transfer is an optimum-size that corresponds to an address boundary of an input/output device.

5. (Original) The method of claim 4, wherein the address boundary corresponds to a strip boundary corresponding to a redundant array of inexpensive disks (RAID) process.

6. (Original) The method of claim 3, further comprising:
storing pending transfer requests; and
determining that the number of pending transfer requests stored is not greater than a queue depth variable before storing a new transfer request.

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7. (Currently Amended) The method of claim 5, further comprising:

gathering performance statistics based on at least one of a number of optimum-size transfers and a largest number of pending requests ~~corresponding to the performance of the method of claim 3;~~ and

modifying the queue depth variable based on the gathered statistics.

8. (Currently Amended) The method of claim 3, wherein a second transfer request is stored on a ~~the~~ pending list, and

wherein the second transfer request is not addressed adjacent to the first transfer request address, and

wherein a third transfer request is coalesced with the first and second transfer requests into the second ~~combined~~ data transfer, the third transfer request being adjacent to the first and second transfer requests.

9. (Currently Amended) A method of transferring data within a computer system, comprising:

receiving transfer requests;

releasing data transfers for processing;

using a computer system coalescing two or more transfer requests into an additional data transfer; and

releasing the ~~an~~ additional data transfer for processing each time a first data transfer completes.

10. (Currently Amended) The method of claim 9 ~~10~~, further comprising:

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determining there are pending transfer requests, wherein the additional data transfer comprises a ~~combined data transfer~~ that ~~includes~~ two or more transfer requests.

11. (Currently Amended) The method of claim 10, further comprising:

determining there are pending transfer requests to coalesce before releasing the additional ~~combined~~ data transfer.

12. (Currently Amended) The method of claim 9 ~~[[4]]~~, wherein releasing the combined data transfer comprises determining that the additional ~~combined~~ data transfer is an optimum-size that corresponds to an address boundary of an input/output device.

13. (Currently Amended) An article comprising a machine-readable medium that stores machine-executable instructions for transferring data, the instructions causing a machine to:

release a first data transfer for processing;

complete a first data transfer;

determine that no data transfers are being processed;

combine two or more transfer requests into a second data transfer; and

release the a second data transfer for processing.

14. (Canceled)

15. (Currently Amended) The article of claim 13 ~~14~~, wherein instructions causing a machine to release comprises instructions causing a machine to determine that the second ~~combined~~ data transfer is an optimum-size that corresponds to an address boundary of an input/output device.

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16. (Original) The article of claim 15, wherein the address boundary corresponds to a strip boundary corresponding to a redundant array of inexpensive disks process.

17. (Original) The article of claim 15, further comprising instructions causing a machine to:

- store pending transfer requests; and
- determine that the number of pending transfer requests stored is not greater than a queue depth variable before storing a new transfer request.

18. (Currently Amended) The article of claim 17, further comprising instructions causing a machine to:

- gather performance statistics corresponding based on at least one of a number of optimum-size transfers and a largest number of pending requests to the performance of the article of claim 13; and
- modify the queue depth variable based on the gathered statistics.

19. (Currently Amended) An apparatus for coalescing transfer requests, comprising:

- a memory that stores executable instructions; and
- a processor that executes the instructions to:
 - release a first data transfer for processing;
 - complete a first data transfer;
 - determine that no data transfers are being processed;
 - combine two or more transfer requests into a second data transfer; and
 - release the a second data transfer for processing.

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20. (Canceled)

21. (Currently Amended) The apparatus of claim 19 ~~20~~, wherein the processor executes instructions to:
determine there are no pending transfer requests to coalesce before releasing the second ~~combined~~ data transfer

22. (Currently Amended) The apparatus of claim 21, wherein releasing comprises determining that the second ~~combined~~ data transfer is an optimum-size that corresponds to an address boundary of an input/output device.

23. (Original) The apparatus of claim 22, wherein the address boundary corresponds to a strip boundary corresponding to a redundant array of inexpensive disks process.

24. (Original) The apparatus of claim 22, wherein the processor executes instructions to:
store pending transfer requests; and
determine that a number of stored transfer requests is not greater than a queue depth variable before storing a new transfer request on the pending list.

25. (Currently Amended) The apparatus of claim 22, wherein the processor executes instructions to:
gather performance statistics based on at least one of a number of optimum-size transfers and a largest number of pending requests ~~corresponding to the performance of the method of claim 23;~~ and
modify the queue depth variable based on the gathered statistics.

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26. (Currently Amended) The apparatus of claim 25, wherein the processor executes instructions to:

store a second transfer request on the pending list, wherein the second transfer request is not addressed adjacent to the first transfer request address; and

coalesce a third transfer request with the first and second transfer requests into the second ~~combined~~ data transfer, the third transfer request being adjacent to the first and second transfer requests.